



Micro-Optics for AR/VR from design to manufacturing

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03-12-2024



Agenda

- 1 **OSG Synopsys Tools for Optical Design and Simulation**
- 2 Micro-Optics Design and System Simulation
- 3 AR/VR Photonic Devices
- 4 Manufacturing Impact and Optical Virtual Fab

Diving into our **OPTICAL SOLUTIONS**

A comprehensive
portfolio

For OPTICAL design
ILLUMINATION design
PHOTONIC design
AUTOMOTIVE design
VIRTUAL prototyping

With proprietary
optimization engines
for **ACCURACY** and
SPEED



CODE V – OPTICAL DESIGN



LIGHTTOOLS – ILLUMINATION DESIGN



RSOFT – PHOTONIC DESIGN

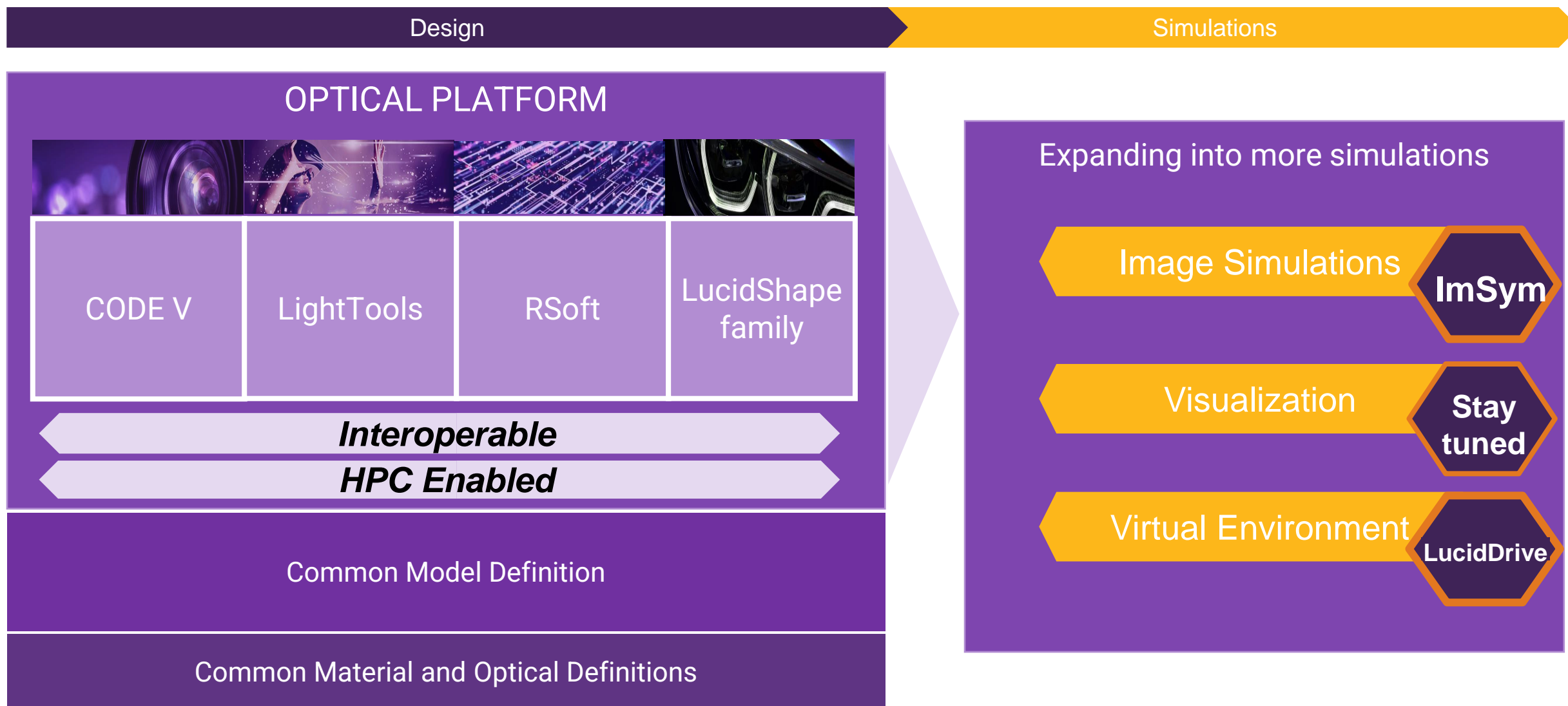


LUCIDSHAPE – AUTOMOTIVE SIMULATIONS



IMSYM – VIRTUAL PROTOTYPING

The Most Advanced Optical Design & Simulations Platform



Accelerate Optical Simulation

Analyzing optical systems can require substantial computing power, as analysis can require **tracing large ray sets, performing high-fidelity FDTD calculations, and performing many other intensive optical analysis mathematics.**



Faster Hardware

- CPU speed
- Multi-core CPUs
- Multi-threading
- GPU



Distributed Hardware

- Multi-CPU computers
- Multiple computers
- Multiple GPUs
- Distributed Scanning



Efficient Flows

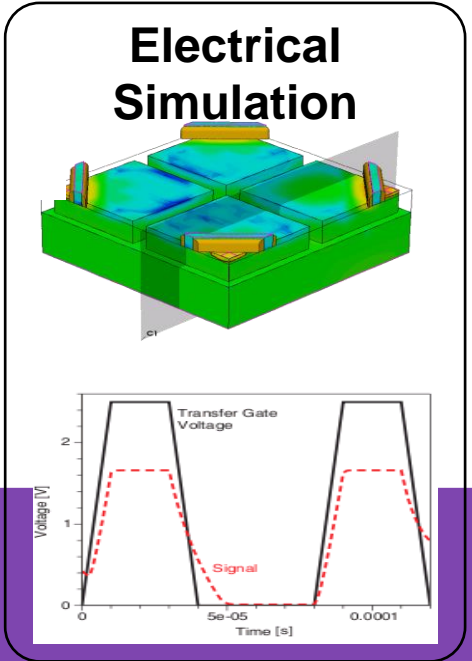
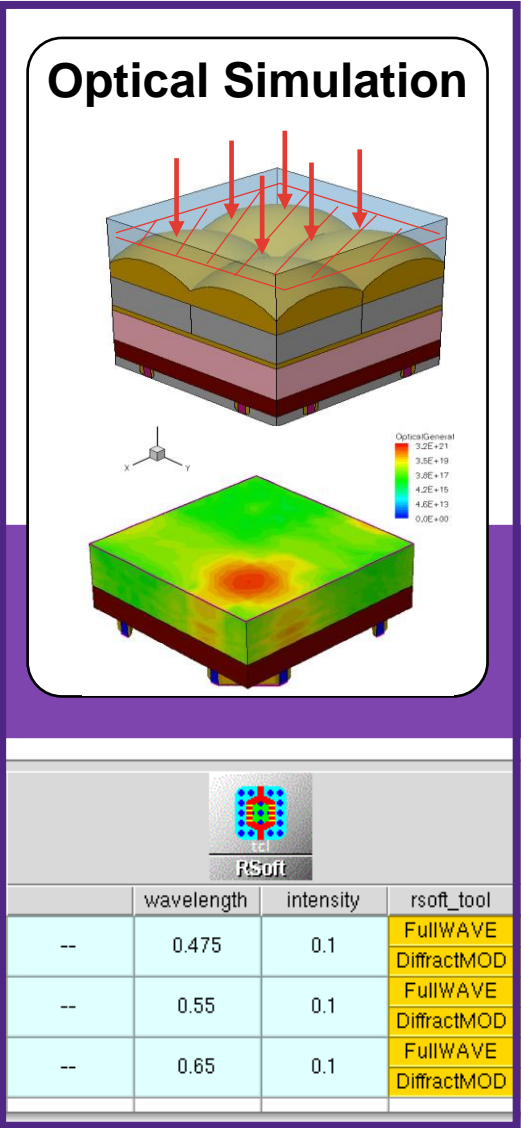
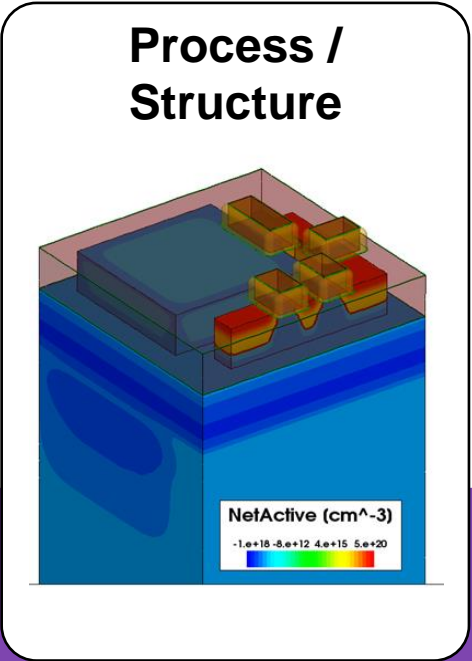
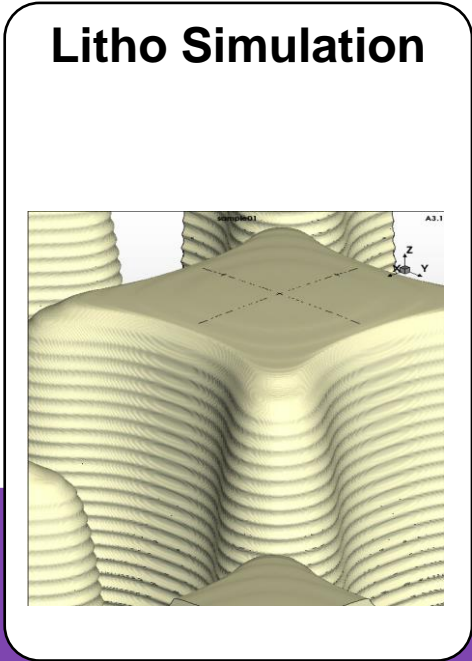
- Learn your tool
- Leverage innovative algorithms
- Self-paced learning options
- Talk to our engineering experts







HOW CAN YOU GET YOUR JOB DONE FASTER?

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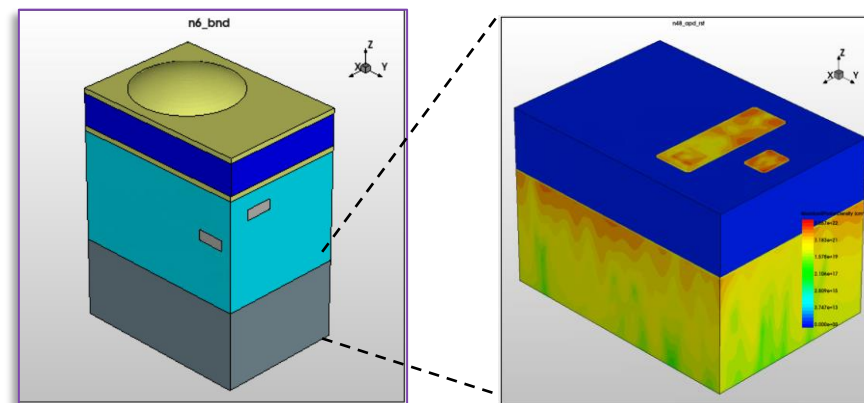
TCAD Sentaurus – RSoft Flow Overview



<div> SPROCESS</div>							<div> SPROCESS</div>	<div> SDE</div>	<div><div> RSoft</div></div>				<div> SDEVICE</div>	<div> SVISUAL</div>	
		pixel	device			cfa		wavelength	intensity	rsoft_tool					
1	--	1	1	--	--	blue	--	0.475	0.1	FullWAVE	--	--			
2															
3															
4															
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6															

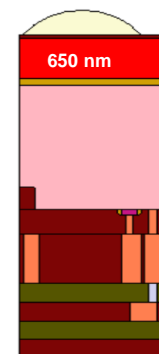
Optical Simulation for CIS

- **DiffractMOD RCWA** and **FuIIWAVE FDTD** to calculate reflection, transmission, absorbed photon density, BSDF, ...
- Micro-Optics variation: Replace Micro-Lens Array (MLA) with a Metalens using **MOD MetaOptic designer**
- Analyze stray light due to reflections from the CIS in a camera system using **LightTools** and **Rsoft BSDF utility**

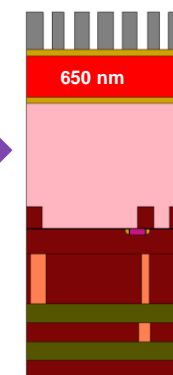


Absorbed Photon Density

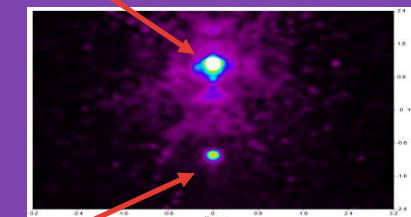
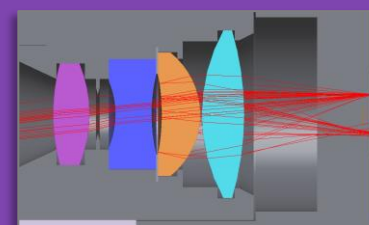
Spherical
Microlens



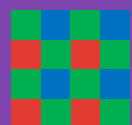
Metalens



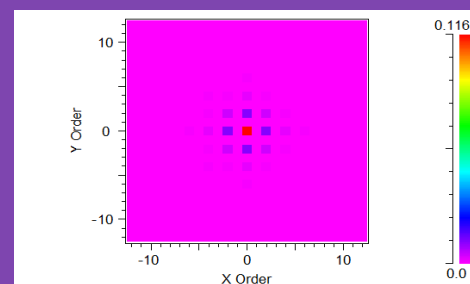
Primary Image Location



Ghost Image



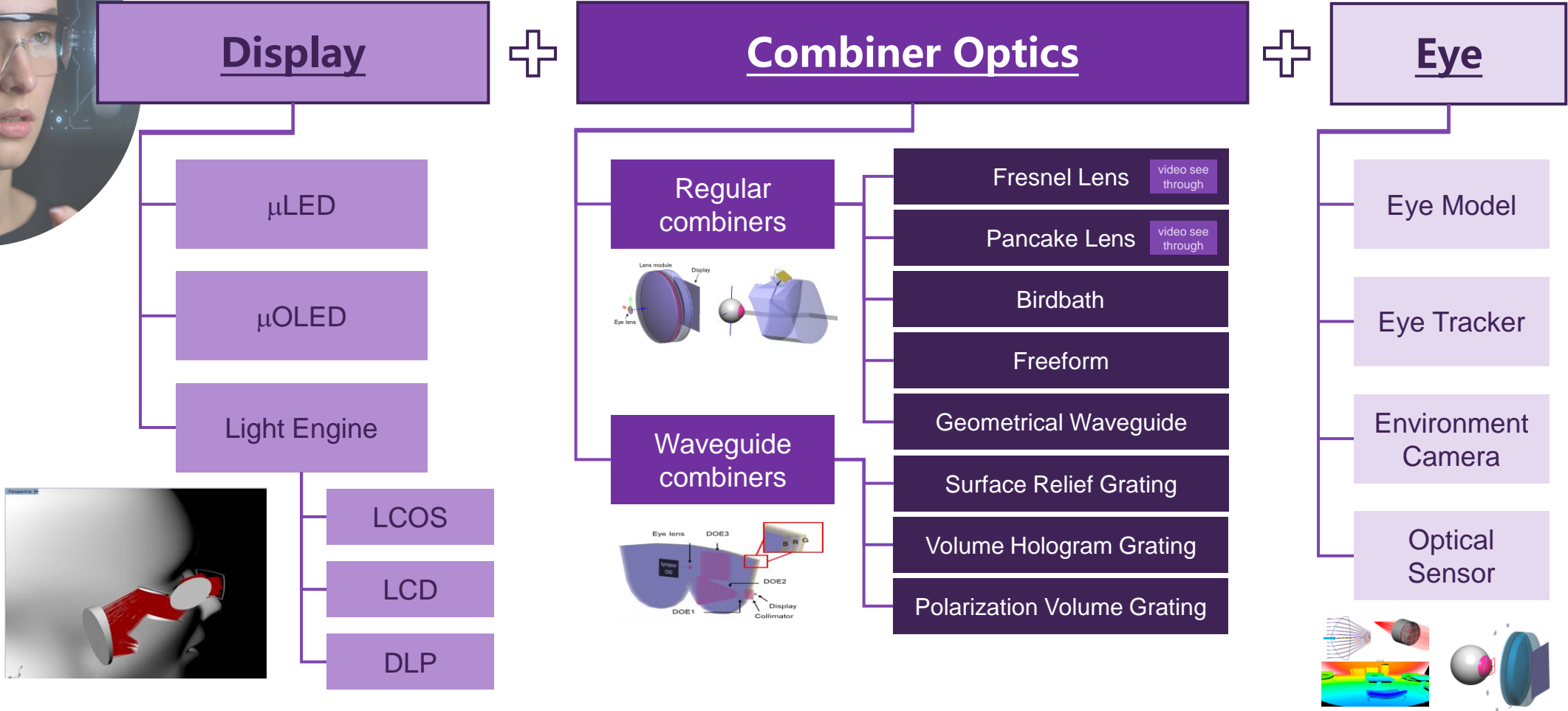
Bayer Pattern
Sensor
Reflection



Agenda

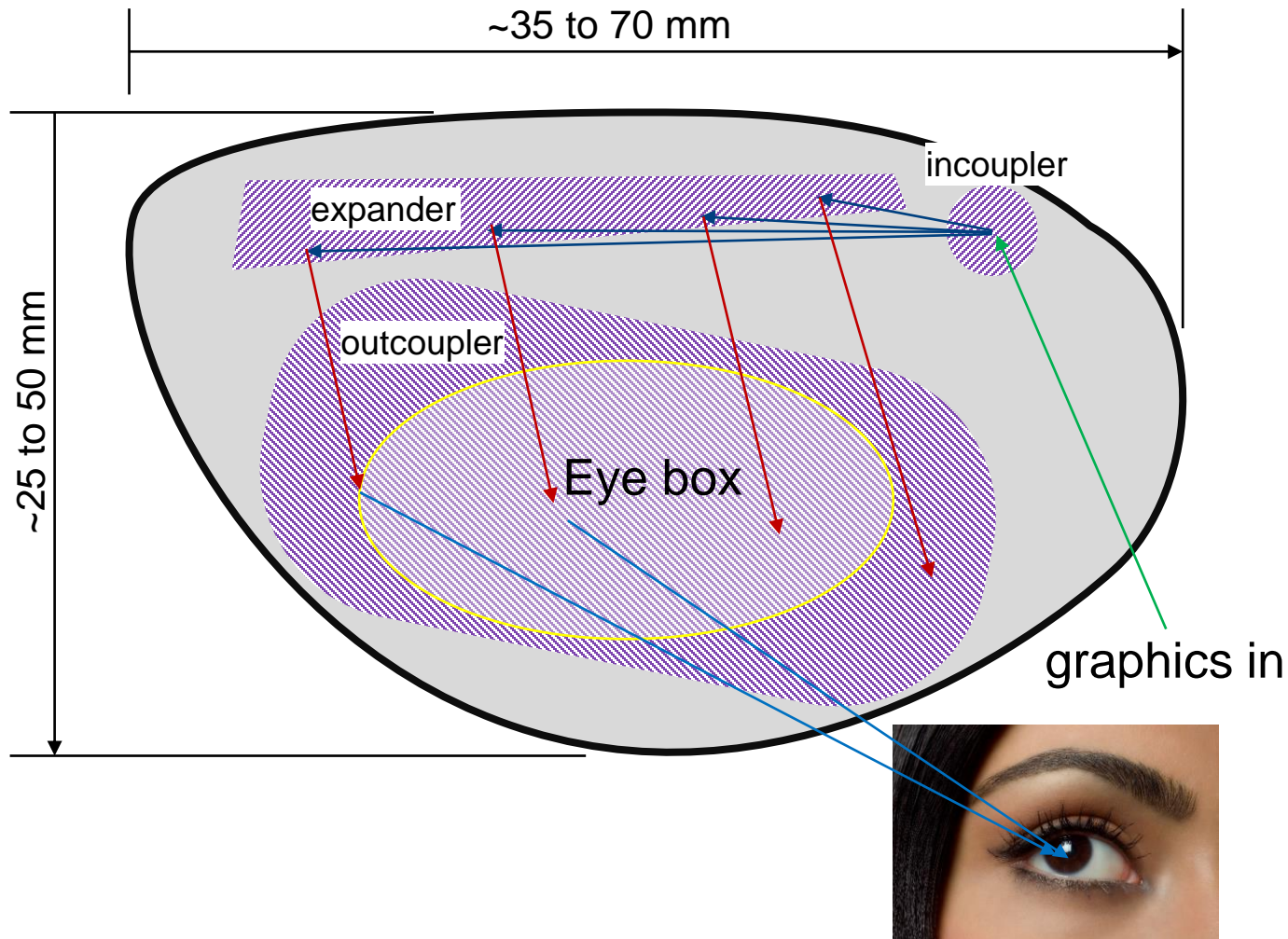
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Key Optical Systems in AR/VR Devices



Waveguide Optical combiner

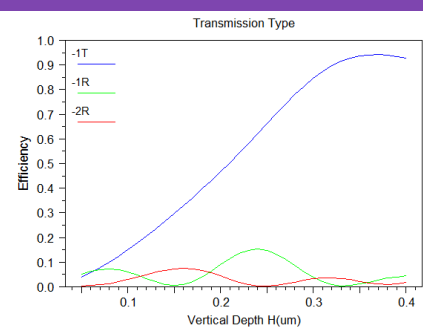
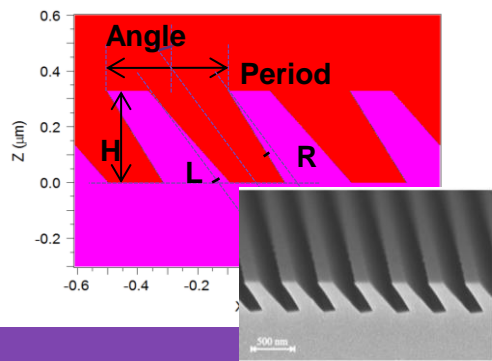
Expands graphics and places them in the eye box



- The **Waveguide Optical combiner** is a series of **waveguides**
 - **Incoupler** transmits the input graphics to the expander
 - **Expander** magnifies the graphics and transmits them to the outcoupler
 - **Outcoupler** overlays the graphics on the real scene for viewing
- The **eye box** is the intended viewing area of the outcoupler
- Surface Relief Gratings (SRG) is a type of Diffractive Optical Elements (DOE) having a periodicity $\sim \lambda$

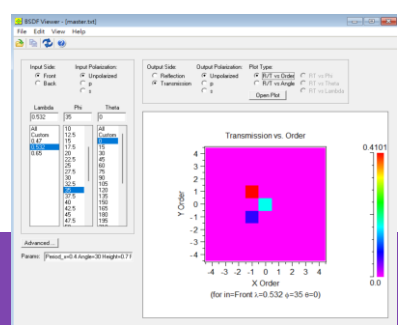
Waveguide AR Design Flow

RSoft DiffractMOD RCWA



1

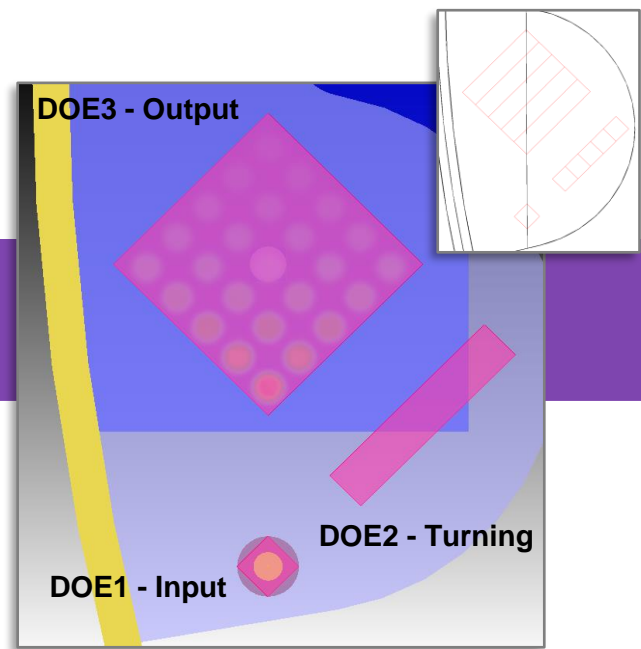
RSoft BSDF Scanning



```
master.txt
OutCoupler2DHexagonH4V5_0_0_0_0_bsdf.dat
OutCoupler2DHexagonH4V5_0_0_0_1_bsdf.dat
OutCoupler2DHexagonH4V5_0_0_0_2_bsdf.dat
OutCoupler2DHexagonH4V5_0_0_0_3_bsdf.dat
OutCoupler2DHexagonH4V5_0_0_0_4_bsdf.dat
OutCoupler2DHexagonH4V5_0_0_0_5_bsdf.dat
OutCoupler2DHexagonH4V5_0_0_0_6_bsdf.dat
OutCoupler2DHexagonH4V5_0_0_0_7_bsdf.dat
```

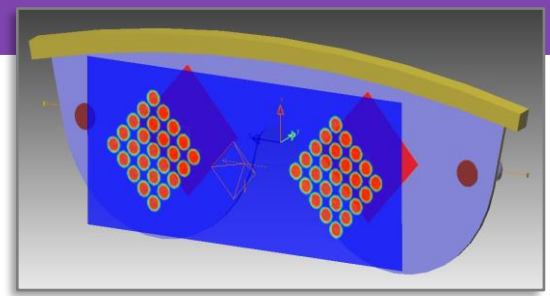
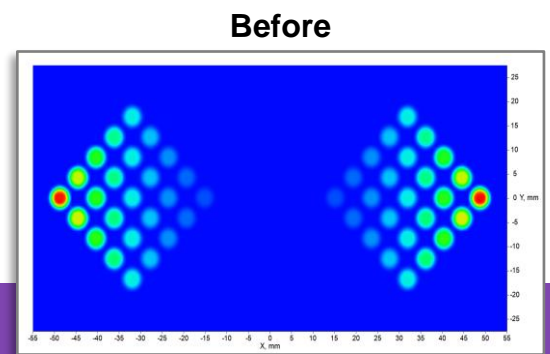
2

LightTools



3

LightTools Optimization (Collimated)



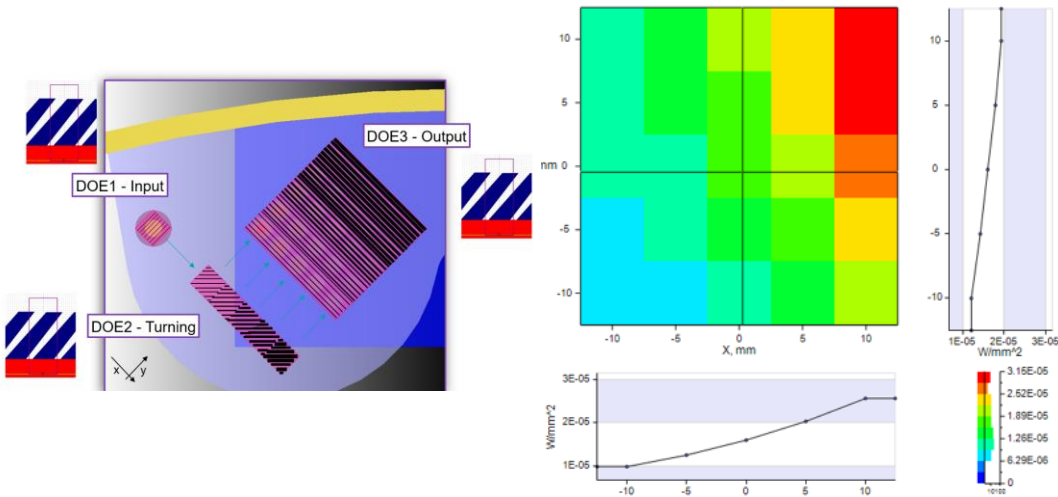
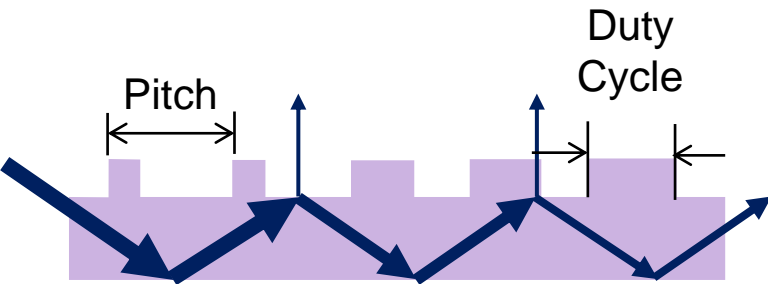
After

4

Workflow

LightTools Design Optimization

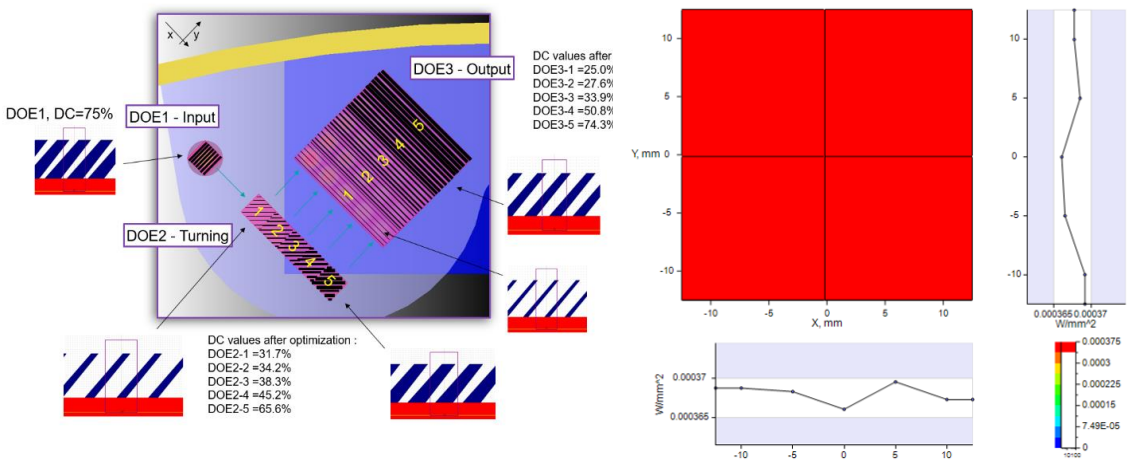
Duty Cycle Variation



Illuminance uniformity map

Before Optimization

Uniform DC (25 % FF)
Efficiency: 1.7%



Illuminance uniformity map

After Optimization

Variable DC (20-70% FF)
Efficiency: 23.5%

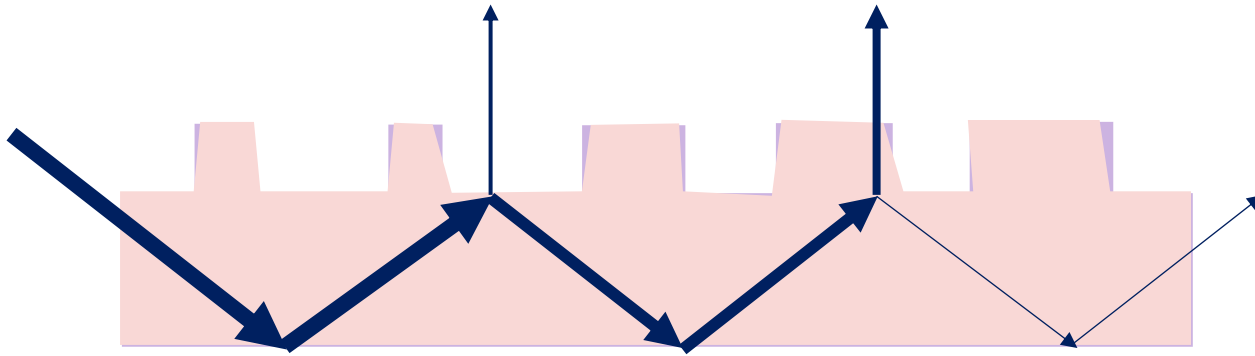
Efficiency: light on the eye box vs incident light

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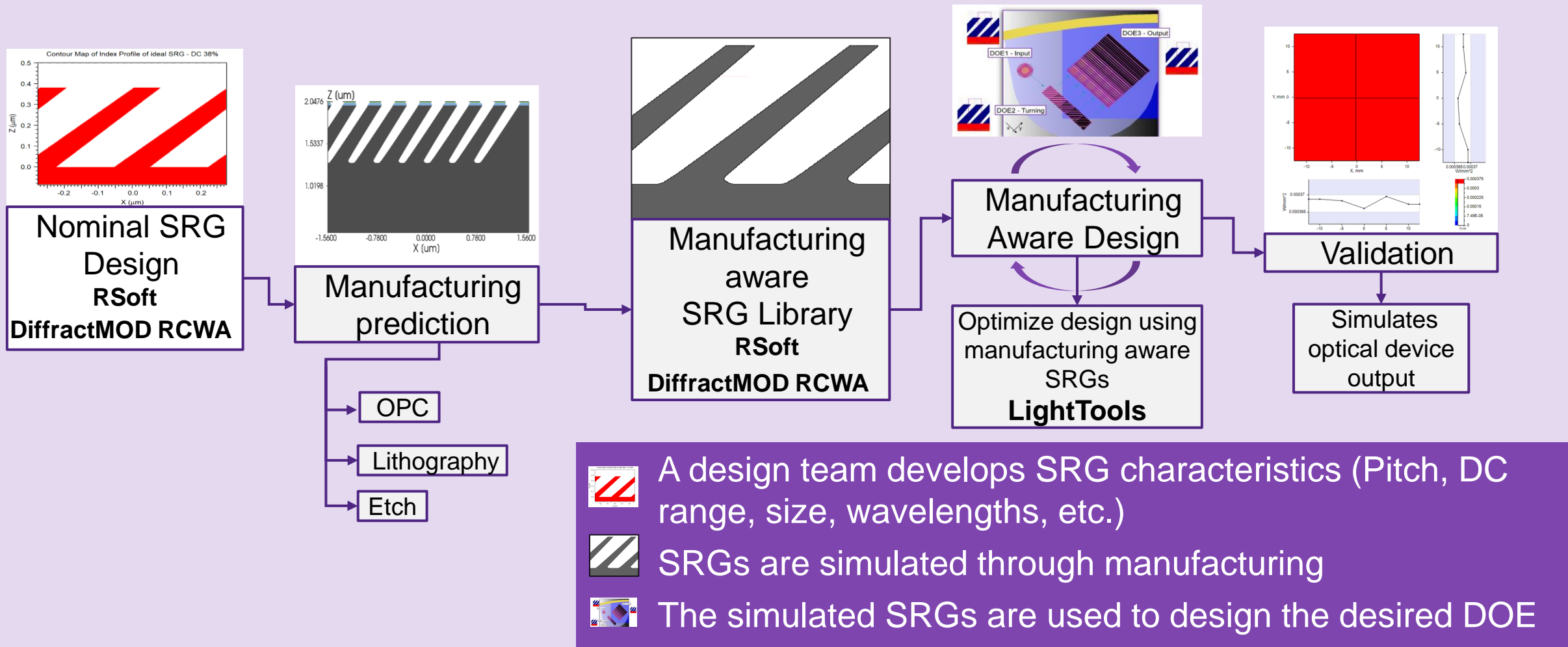
SRG After Manufacturing

Manufacturing artifacts may dramatically impact device performance



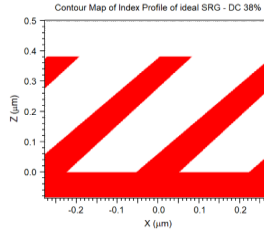
- Manufacturing produces a device that is not ideal (as the designer drew it)
 - Sidewall angles
 - Over etching
 - Rounding corners and at trench bottom
- These artifacts may result in:
 - Eye box uniformity degradation
 - Intensity loss
 - Blurred images
 - Failed devices

Manufacturing Aware Design Flow Targets First Print Correct Design

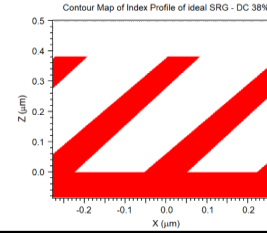


Optimizing the Design: Applying Manufacturing Simulation

Grating profile used in design:



Ideal

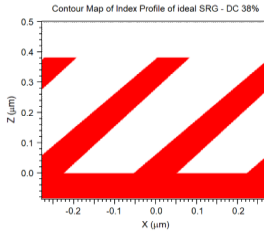


Ideal



Realistic

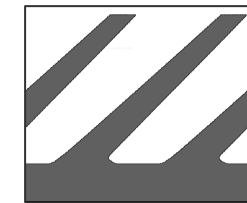
Grating profile as-manufactured:



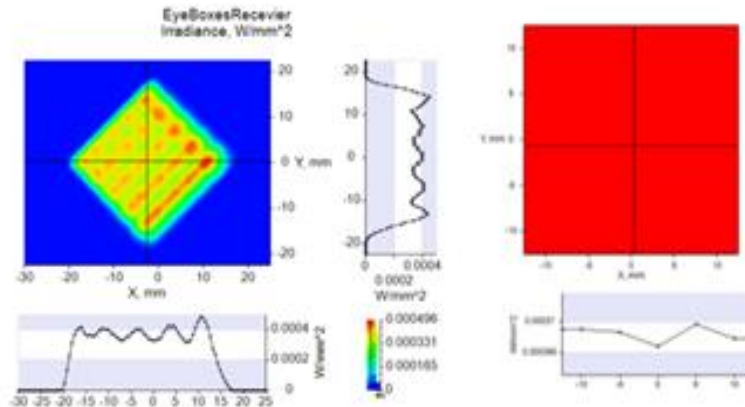
Ideal



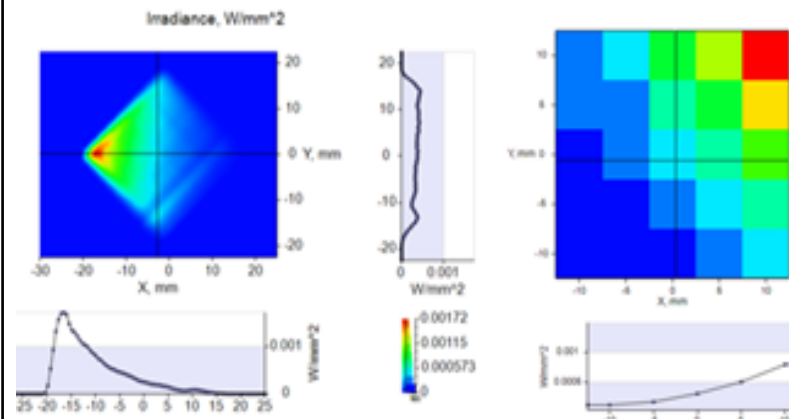
Realistic



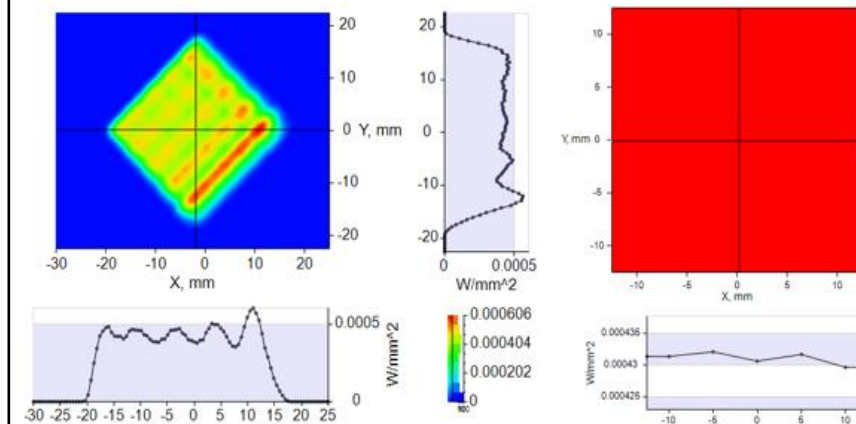
Realistic



Efficiency: 23.5%



Efficiency: 17%



Efficiency: 27.4%

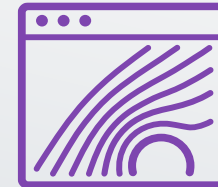
Illuminance uniformity on eye box

Conclusion

- Streamline collaboration between teams to provide useful and comprehensive flows
- Flexibility and interoperability between tools for a better system design simulation
- Cost and time reduction:
 1. Innovative algorithm
 2. Cluster computation
 3. Hardware acceleration
 4. Optical Virtual Fab



Streamline
collaboration
between teams



Flexibility



Cost/Time Reduction

Thank you

A close-up of a camera lens with a bright light reflecting off its glass elements.

CODE V – OPTICAL DESIGN

A hand reaching out towards a field of glowing particles and light rays in a dark space.

LIGHTTOOLS – ILLUMINATION DESIGN

A complex, glowing circuit board with various colored lines and dots.

RSOFT – PHOTONIC DESIGN

A close-up of a car's engine and suspension components, showing mechanical details.

LUCIDSHAPE – AUTOMOTIVE SIMULATIONS

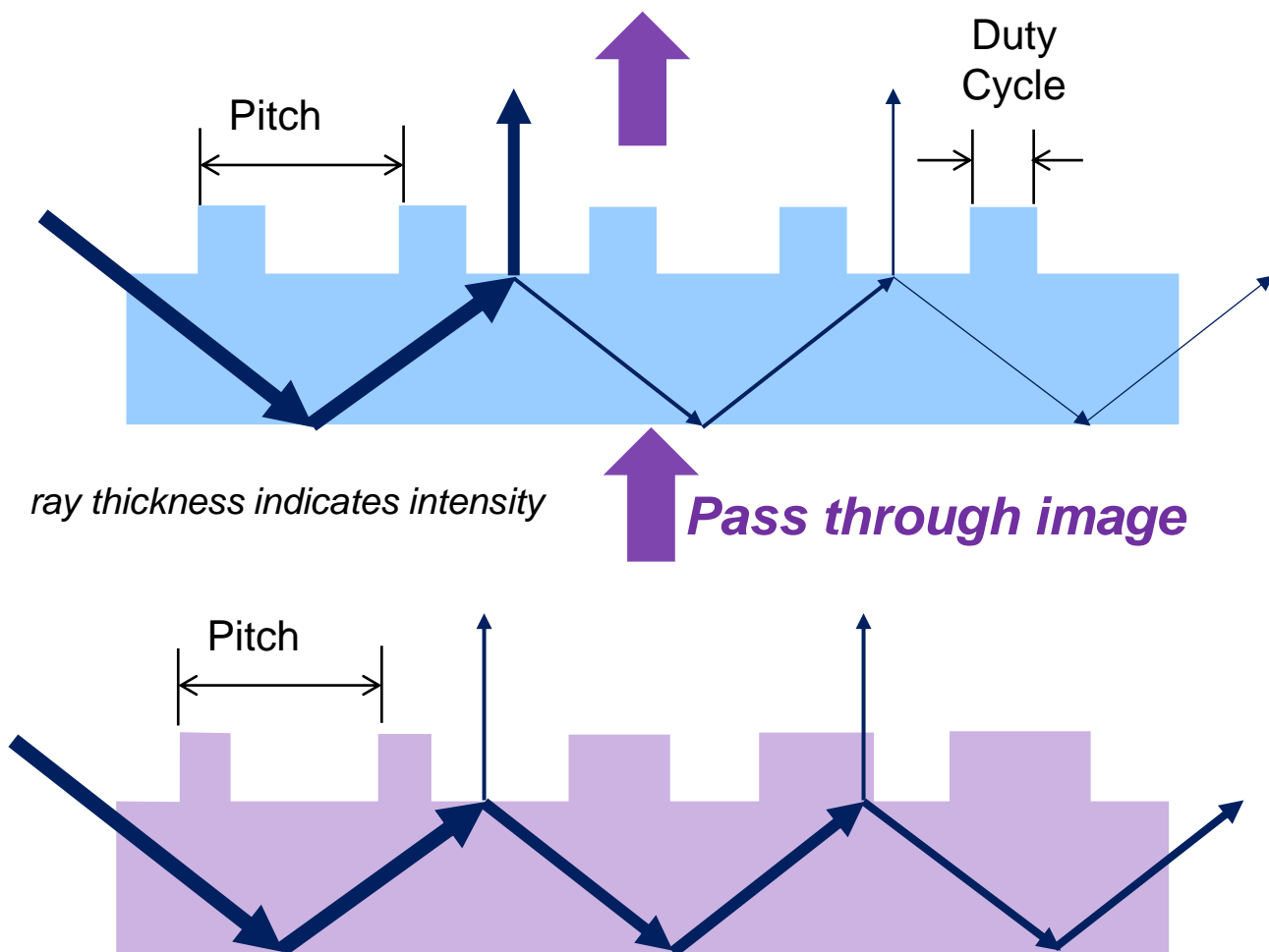
A collection of 3D rendered mechanical parts, including lenses and housings, in a dark environment.

IMSYM – VIRTUAL PROTOTYPING

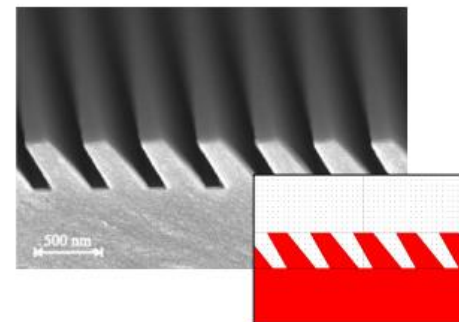
SYNOPSYS®

Surface Relief Grating Waveguide

An AR/VR strategy to combine environmental and graphical images

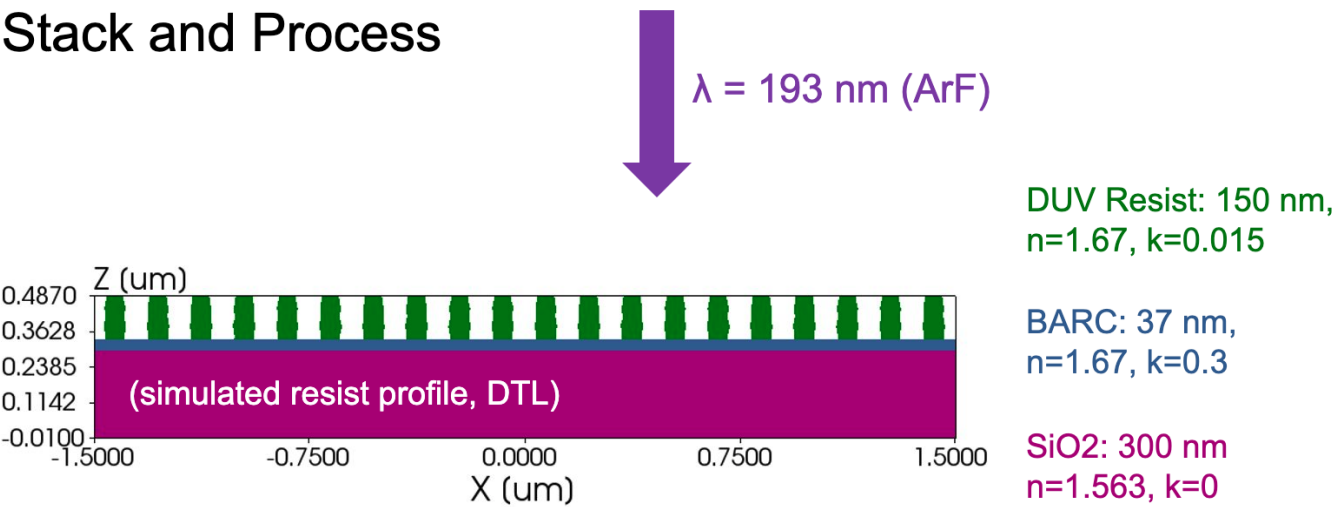


- SRGs are periodic structures of constant **Pitch**
- Pitch governs the wavelength(s) of light manipulated by the SRG
- The **Duty Cycle (DC)** governs reflected images
- **Filling Factor (FF)** = $DC/Pitch$
- DC is varied to balance output intensity
- Slanted gratings are more efficiently directing light than 90° gratings

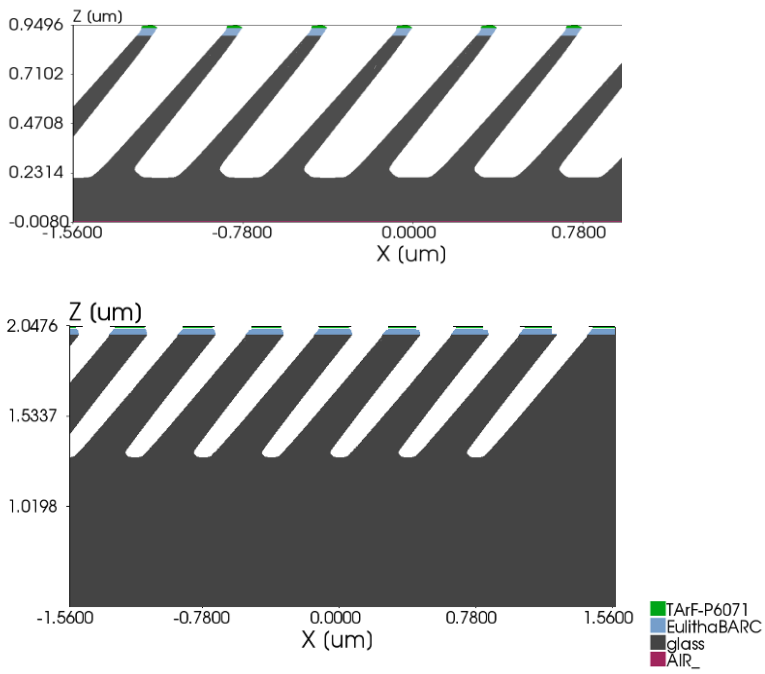
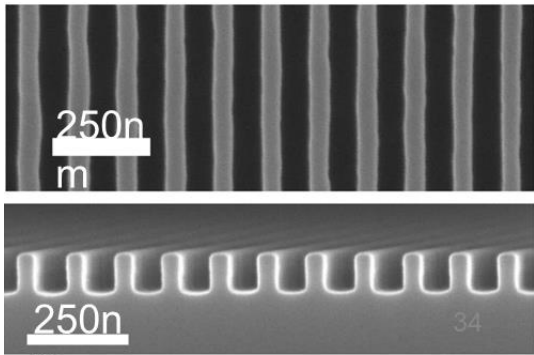


Modeling SRG Manufacturing

Stack and Process



- 193 nm Displacement Talbot lithography imaged SRG patterns
- RIE etch simulation to transfer pattern into SiO₂ parameters
- In collaboration with  Eulitha



WOOLEY, Kelsey, CHALONY, Maryvonne, DAWES, Andrew MC, et al. Simulating use of displacement Talbot lithography for high volume AR waveguide manufacturing. In : DTCL and Computational Patterning III. SPIE, 2024. p. 308-318.